

The ATE Integration Solution for your ISP Needs



FR01AT0

High-Performance, Standalone
In-System Programmer for ATE

- Easily integrates into Agilent, Teradyne, SPEA, Genrad, TRI and others ATEs
- Fast programming algorithms
- Non-intrusive programming
- Data retention guaranteed



SMH
Technologies

FR01AT0

High-Performance, Standalone In-System Programmer for ATE

FlashRunner FR01AT0 is a member of the FlashRunner series of a high-performance, standalone In-System Programmers specific for Flash-based microcontrollers and serial memories. FlashRunner FR01AT0 is targeted at production environments and can work either in full standalone mode or controlled by a host system.

FlashRunner FR01AT0 is specifically designed for an easy integration with all in-circuit and functional test systems (ATEs), like Agilent, Teradyne, SPEA, Genrad, TRI and others.

A relay circuitry allows each of the FlashRunner ISP lines to be independently disconnected from the target system, routing I/O lines coming from the ATE system to the target system instead (see diagram).

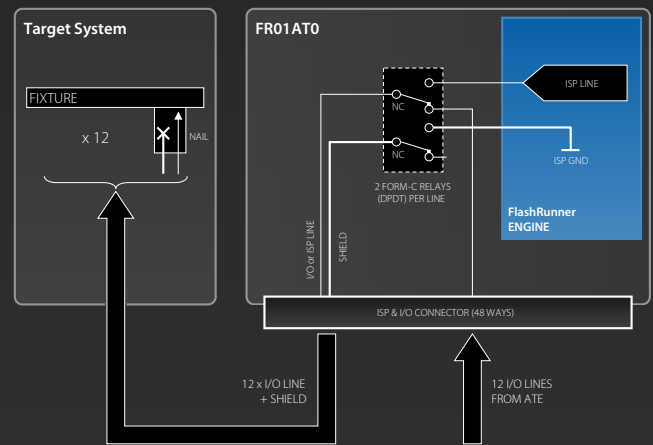


Main Features

- Fastest programming algorithms (as fast as target device's memory technology limit), approved by silicon manufacturers
- Easy ATE integration
- Standalone operations (projects and code images stored on a memory card)
- Controllable by ATE through optoisolated LAN, RS-232 or parallel control lines
- Multiplexing on ISP lines coming from ATE or FlashRunner to the fixture
- Flexible, fully configurable
- Compact and robust design for production environments

Advanced Features

- Galvanic isolation (relay disconnection) for all I/O ISP lines
- Data retention guaranteed thanks to power and current monitoring during programming
- Power voltage monitoring has programmable threshold and pulse width
- Over-current monitoring on programmable power supply lines



Block Diagram

Specifications

Hardware

- 9 to 24V power supply input
- Galvanic isolation (relay disconnection) with shield to fixture for all I/O ISP lines:
 - Five digital I/O lines
 - Two digital I/O or analog output lines
 - Two programmable output voltages
 - One analog input line
 - One programmable clock output
- Secure Digital memory card (up to 2 GB)
- 512 bytes on-board dynamic memory
- On-board timekeeper and calendar for time-stamped log file
- Three optoisolated command inputs (START, STOP, RELAY)
- Three optoisolated status outputs (FAIL, PASS, BUSY)
- Five project selection lines
- Optoisolated RS-232/Ethernet channels

Software

- Fully autonomous standalone mode thanks to SD memory card (FAT16)
- Controllable by any host system through a terminal utility and simple ASCII protocol
- 32 hardware-selectable and unlimited software-selectable projects
- Interface Library DLL to control the instrument from within user written applications
- Optional Data Protection System to make the contents of the binary file to be programmed to the target device not readable (and not duplicable) by non-authorized people
- Time-stamped log files
- Erase, blank check, program, read, verify, oscillator trimming, etc.

Dimensions

- 130 x 74 x 42 mm (with top panel)
- 130 x 74 x 35 mm (without top panel)



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